



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/032,743	10/25/2001	Jesus Palomino Echartea	10559-623001/P12874	6401
20985	7590	04/19/2004	EXAMINER	
FISH & RICHARDSON, PC 12390 EL CAMINO REAL SAN DIEGO, CA 92130-2081			VO, TIM T	
		ART UNIT		PAPER NUMBER
		2112		4
DATE MAILED: 04/19/2004				

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/032,743	ECHARTEA ET AL.
Examiner	Art Unit	
Tim T. Vo	2112	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 25 October 2001.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-30 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____.
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date <u>3</u> .	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____.

Part III DETAILED ACTION

Notice to Applicant(s)

1. This application has been examined. Claims 1-30 are pending.

Drawings

2. Formal drawings will be required when the application is allowed.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. § 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1-30 are rejected under 35 U.S.C. § 102(b) as being anticipated by Alaimo et al. (WO 00/44115) referred hereinafter "Alaimo".

As for claims 1, 11 and 21, Alaimo teaches a bus framer comprising: an engine, which extracts information from a frame of data being transmitted over a time-division multiplexed bus; and a processor which retrieves the information from the engine over an internal bus and forwards the information. (see figure 5, engine processor 102, management processor 100, TDM bus 32, framers 24 and page 7 lines 3-30 and page 18 line 17 to page 20 line 7, wherein the plurality DS1S of the telecommunication device 10 receives plurality of framers 24 which is shown in figure 2, Figure 5, discloses detail of the telecommunication device 10 comprising controller card 26, 30 each comprising

management processor 100, engine processor 102 to performing multi tasks such as extracting information from framer 24 over the TDM multiplexed bus 32 and monitoring process state information on the framer 24 (page 23 lines 1-5)).

As for claims 2, 12 and 22, Alaimo teaches a mapper which maps the frame of data on the time-division multiplexed bus to a read/write bus (see page 4 line 28 to page 5 line 19); and a functional module which receives data from the read/write bus and which handles the data (see page 4 line 28 to page 5 line 19).

As for claims 3, 13 and 23, Alaimo teaches, wherein the time-division multiplexed bus, the internal bus, and the read/write bus all run off the same clock (see page 19 lines 16-19).

As for claims 4, 14 and 24, Alaimo teaches a storage medium for storing the information in a database; and an interface module which provides a link to an external device; wherein the processor forwards the information to at least one of the storage medium and the interface module (see page 23 lines 12-16).

As for claims 5, 15 and 25, Alaimo teaches a framing engine which generates the frame and outputs the frame to the time-division multiplexed bus (see page 24 lines 16-23).

As for claims 6, 16 and 26, Alaimo teaches, wherein the framing engine stores the frame in memory prior to outputting the frame, the frame in memory comprising: a data structure having blocks arranged in N rows and M columns, where N and M are integers that are greater than one, a block including data corresponding to a destination

Art Unit: 2112

port and a time slot for the data (see page 15, wherein the data structure in a buffer having rows and columns).

As for claims 7, 17 and 27, Alaimo teaches, wherein the engine comprises one of (a) a signaling engine which extracts signaling information from the frame, (b) an alarms engine which extracts alarm codes from the frame, (c) a facility data link engine which extracts messages from the frame, and (d) an overhead engine which extracts overhead bits from the frame (see page 8-19).

As for claims 8, 18 and 28, Alaimo teaches, wherein the functional module comprises one of (a) a scalar high-speed bus (bus 32), (b) a slip buffer which stores data temporarily to accommodate frequency and phase differences between a clock of the bus framer and external clock domains (buffer 120), (c) a system backplane with a connection to an external device (see back plane 22), (d) a bit error rate testing generator/analyizer (see page 5 line 26 to page 6 line 2), and (e) a high-speed data link controller (see page 16 lines 17-22).

As for claims 9-10, 19-20 and 29-30, Alaimo teaches a read/write bus; plural functional modules which communicate with the engine via the read/write bus; and an arbiter which regulates access of the plural functional modules to the read/write bus (see page 22 lines 14-20).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim T. Vo whose telephone number is 703-308-5862. The examiner can normally be reached on 7:30-5:00.

Art Unit: 2112

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 703-305-4815. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Tim T. Vo
Primary Examiner
Art Unit 2112

4/15/04